

In re the Application of:
KOMIYAMA et al.
Serial No. 09/776,528
Filed: February 4, 2001

Group Art Unit: 2814

Examiner: Ha, Nathan W.

For: METHODS FOR MANUFACTURING
SEMICONDUCTOR CHIPS, METHODS FOR
MANUFACTURING SEMICONDUCTOR
DEVICES, SEMICONDUCTOR CHIPS,
SEMICONDUCTOR DEVICES, CONNECTION
SUBSTRATES AND ELECTRONIC DEVICES



Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ___ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
- ___ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
- ___ It is believe that no additional fee is required.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO PREVIOUSLY PAID FOR	PRESENT EXTRA RATE	ADDIT. FEE	OR	ADDIT. RATE	ADDIT. FEE
TOTAL	26 MINUS	25 =	1 x	\$9	OR	x 18	\$18
INDEP CLAIMS *	7 MINUS	7 =	0 x	\$43	OR	x 86	\$0
___ FIRST PRESENTATION OF MULTIPLE DEP. CLAIM			+	\$145	OR	+ 290	\$0
			TOTAL	\$0	OR	TOTAL	\$18

___ Please charge Deposit Account No. 50-0585 the amount of \$___ to cover the extension fee and also the amount of \$___ to cover the claim fee. A duplicate copy of this sheet is enclosed.

___ A check in the amount of \$___ to cover the extension fee is enclosed.

X A check in the amount of \$ 18 to cover the extra claim fee is enclosed.

X The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. No. 50-0585. A duplicate of this sheet is enclosed.

X Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

X Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

Alan S. Raynes
Alan S. Raynes

Dated: June 18, 2004

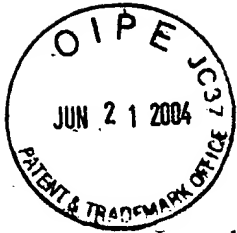
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Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 18, 2004.

Alan S. Raynes
Alan S. Raynes

June 18, 2004
Date



15.32/5685

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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SEMICONDUCTOR CHIPS, METHODS FOR)	
MANUFACTURING SEMICONDUCTOR)	
DEVICES, SEMICONDUCTOR CHIPS,)	<u>AMENDMENT</u>
SEMICONDUCTOR DEVICES, CONNECTION)	
SUBSTRATES AND ELECTRONIC DEVICES)	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sirs:

This Amendment is filed in response to the Office Action dated March 18, 2004. Please enter and consider the following.

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